

ABSTRACT OF THE DISCLOSURE

A reset circuit of a timing controller includes: a transistor including an emitter connected to a first node, a base connected to a second node and a collector connected to a third node, a digital input voltage (DVCC) being applied to the first node; a first resistor connected between the first and second nodes; a second resistor connected between the second node and a fourth node, the fourth node being grounded; a third resistor connected between the third and fourth nodes; a fourth resistor connected between the third node and a fifth node, the fifth node being connected to an input terminal of the timing controller; and a capacitor including a first electrode connected to the fifth node and a second electrode grounded.